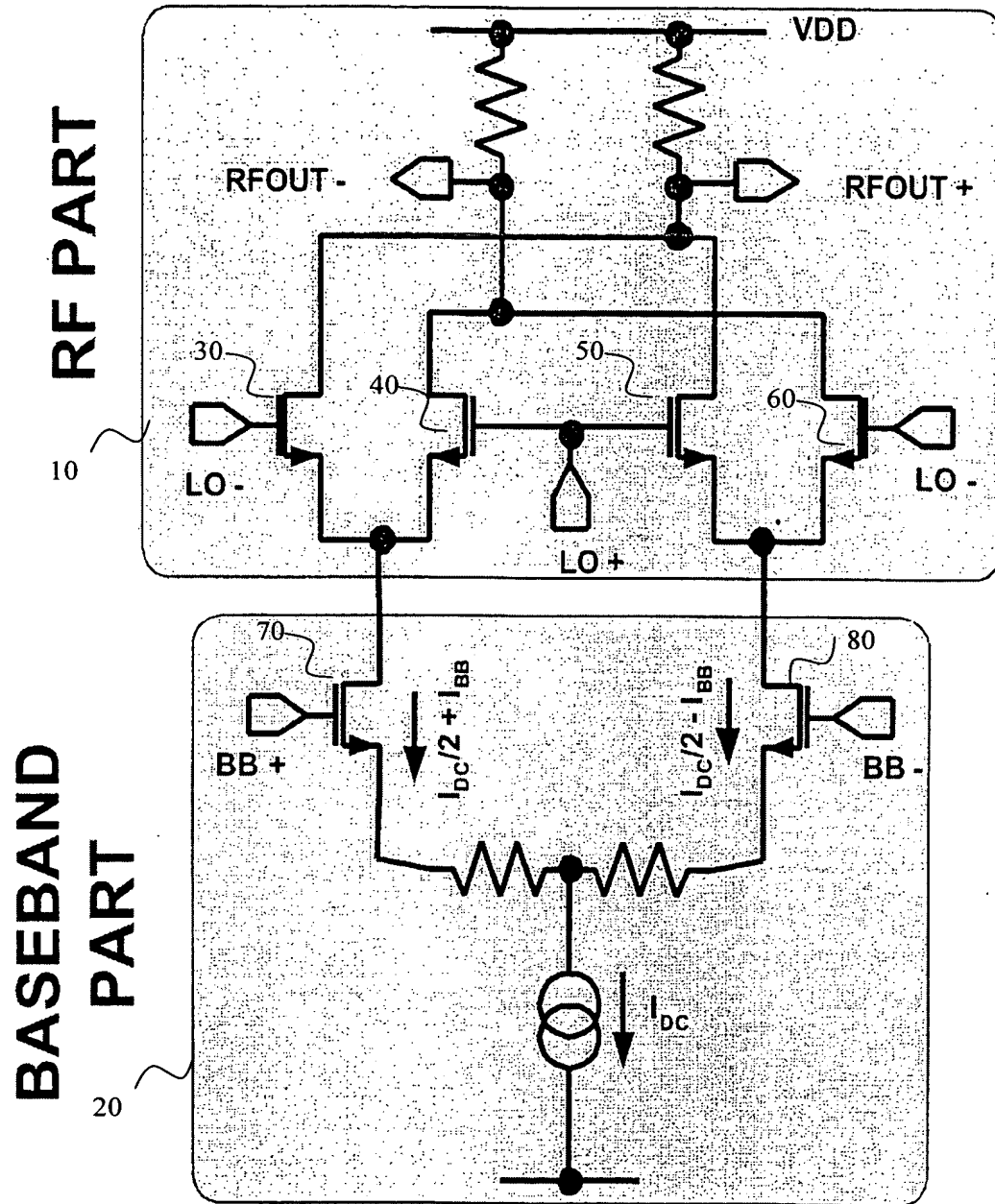


FIG 2 PRIOR ART





## Output signal

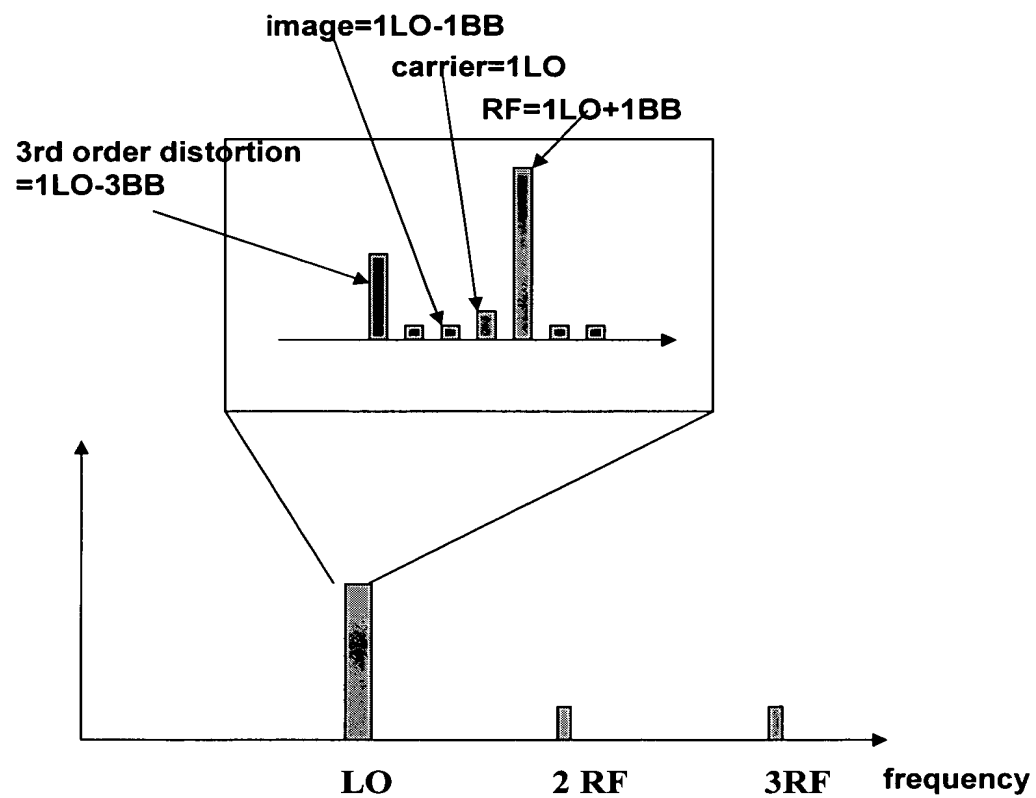


Fig. 6

At low impedance node

